What Is Claimed Is:

1. A device for controlling a memory, in particular for data to be transported via a network
   bridge, including the following features:
   - the memory (F) is subdivided into a plurality of memory areas or is made up of a
     plurality of individual memories;
   - the memory areas/individual memories are designed to be configurable independently
     of each other;
   - means (MK) are provided both for the preadjustment of the individual memory
     areas/individual memories and for dynamic modification with respect to the size
     and/or the number of the individual memory areas/individual memories, during
     operation.

2. The device as recited in Claim 1,
   wherein the memory (F) is connected via an interface (CIF) to means (MK) for
   configuring and controlling the network bridge so as to query data, analyze data, and
   obtain parameters for operating the memory (F) on the basis of the analysis, in
   particular with respect to its memory division and occupancy by different types of
   data.

3. The device as recited in Claim 1 or 2,
   wherein the memory (F) is able to be connected to an external memory (EF) via an
   additional interface (MIF) in order to thereby increase the overall size of the memory
   (F).

4. The device as recited in one of Claims 1 through 3,
   wherein the means (MK) for configuring and controlling the network bridges are able
   to be connected to additional functional blocks of the network bridge via additional
   interfaces (I) in order to collect and analyze data and in order to modify parameters
   within the functional blocks, including the memory (F), as a function thereof.

5. The device as recited in one of Claims 1 through 4,
   wherein the means (MK) for configuring and controlling the network bridge are made
   up of a software layer within the network bridge architecture.